Enronn	ent No:		
	C.U.SHA	H UNIVERSIT	\mathbf{Y}
	Summer	Examination-2018	}
Subject	Name: Computer Organizati	on & Architecture	
Subject	Code: 4TE04COA1	Branch: B.Tech (CE)	
Semeste	r: 4 Date :01/05/2018	3 Time: 10:30 To 01:30	Marks: 70
(2) (3) (4) Q-1 a) b) c) d)	Use of Programmable calculato Instructions written on main and Draw neat diagrams and figures Assume suitable data if needed. Define the following terms: IR register Computer organization Function on PC RISC and CISC Scalar processor SIMD and MIMD.		-

Attempt any four questions from Q-2 to Q-8

Q- 2		Attempt all questions	(14)
	(a)	What are Three state buffers? Design common bus system using three state	(7)
		buffers.	
	(b)	Explain Different types of selective operations.	(7)
Q-3		Attempt all questions	(14)
	(a)	Explain Direct Address, Indirect Address and effective address with example.	(7)
	(b)	Draw Flow Chart of Instruction Cycle and explain.	(7)

Q-4		Attempt all questions	(14)
	(a)	Draw and Explain Common Bus System using basic register.	(7)
	(b)	Explain different types of instruction in basic computer with example.	(7)
Q-5		Attempt all questions	(14)
	(a)	Design one stage adder and logic circuit.	(7)
	(b)	Write a short note on Addressing mode.	(7)
Q-6		Attempt all questions	(14)
	(a)	Draw and Explain General Register Organization.	(7)
	(b)	What is Assembler? Explain first pass of assembler.	(7)
Q-7		Attempt all questions	(14)
_	(a)	Write a short note on Parallel Processing.	(7)
	(b)	Explain Instruction Pipeline with Architecture.	(7)
Q-8		Attempt all questions	(14)
-	(a)	Explain Stack Organization in the form of Register Stack.	(7)
	(b)	Explain vector processing and superscalar processor.	(7)

