

C.U.SHAH UNIVERSITY

Summer Examination-2018

Subject Name: Computer Organization & Architecture

Subject Code: 4TE04COA1

Branch: B.Tech (CE)

Semester: 4

Date :01/05/2018

Time : 10:30 To 01:30

Marks : 70

Instructions:

- (1) Use of Programmable calculator & any other electronic instrument is prohibited.
 - (2) Instructions written on main answer book are strictly to be obeyed.
 - (3) Draw neat diagrams and figures (if necessary) at right places.
 - (4) Assume suitable data if needed.
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- Q-1 Define the following terms: (14)**
- a) IR register
 - b) Computer organization
 - c) Function on PC
 - d) RISC and CISC
 - e) Scalar processor
 - f) SIMD and MIMD.
 - g) Subroutine
 - h) Zero-address Instruction
 - i) Software Interrupt
 - j) BSA
 - k) Assembly level language.
 - l) SPA
 - m) Circular shift microoperation
 - n) Pseudoinstruction

Attempt any four questions from Q-2 to Q-8

- Q-2 Attempt all questions (14)**
- (a) What are Three state buffers? Design common bus system using three state buffers. (7)
 - (b) Explain Different types of selective operations. (7)
- Q-3 Attempt all questions (14)**
- (a) Explain Direct Address, Indirect Address and effective address with example. (7)
 - (b) Draw Flow Chart of Instruction Cycle and explain. (7)



